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2.0_AM176220C2 intial code
void InitMain(void) //TFT-LCD SPI IF Initial funciton//
{

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    HX8306Insw(0x0007,0x0004);
    HX8306Insw(0x0012,0x0000);
    HX8306Insw(0x0013,0x0000);
    delay(40);
    HX8306Insw(0x0001,0x011B);
    HX8306Insw(0x0002,0x0500);
    HX8306Insw(0x0003,0x6230);
    HX8306Insw(0x0004,0x0000);
    HX8306Insw(0x0005,0x0000);
    HX8306Insw(0x000B,0x0000);
    HX8306Insw(0x0011,0x0001);
    HX8306Insw(0x0012,0x0008);
    HX8306Insw(0x0013,0x1517);
    HX8306Insw(0x0010,0x4040);
    delay(40);
    HX8306Insw(0x0012,0x0018);
    delay(40);
    HX8306Insw(0x0013,0x3D1D);
    delay(40);
    HX8306Insw(0x0021,0x00AF);
    HX8306Insw(0x0030,0x0000);
    HX8306Insw(0x0031,0x0000);
    HX8306Insw(0x0032,0x0000);
    HX8306Insw(0x0034,0x0000);
    HX8306Insw(0x0035,0x0000);
    HX8306Insw(0x0036,0x0000);
    HX8306Insw(0x0037,0x0001);
    HX8306Insw(0x0038,0x0000);
    HX8306Insw(0x0039,0x0000);
    HX8306Insw(0x0040,0x0000);
    HX8306Insw(0x0041,0x00EF);
    HX8306Insw(0x0042,0xDB00);
    HX8306Insw(0x0043,0xDB00);
    HX8306Insw(0x0044,0xAF00);
    HX8306Insw(0x0045,0xDB00);
    HX8306Insw(0x0007,0x0105);
    delay(40);
    HX8306Insw(0x0007,0x0125);
    HX8306Insw(0x0007,0x0077);
    HX8306Insw(0x0001,0x191B);
    HX8306Insw(0x0003,0x1230);
    HX8306Insw(0x0008,0x0202);
    HX8306CurSet(0,0);
    HX8306Insw(0x000c,0x0111);
    writeSPI(0x70,0x0022);

```

```

}

```

```

void HX8306Insw(int offset,int Dat16bit)
{
    writeSPI(0x70,offset);
    writeSPI(0x72,Dat16bit);
    delay(100);

```

```

}

```

```

void writeSPI(char SPI_ID,int SPIData)
{
    int i; int j;

```

2.0_AM176220C2 intial code

```
SCL=1;
LCS=0;

for(i=0;i<8;i++)
{
    if(SPI_ID & 0x80)
        SDI=1;
    else
        SDI=0;
    SCL=0;
    SCL=1;
    SPI_ID<<=1;
}
for(j=0;j<16;j++)
{
    if(SPIData & 0x8000)
        SDI=1;
    else
        SDI=0;
    SCL=0;
    SCL=1;
    SPIData<<=1;
}
    LCS=1;
}

void HX8306CurSet(char X,char Y)
{
    static int Reg1;
    Reg1=0x00ff & Y;
    Reg1=Reg1<<8;
    Reg1= Reg1|X;          // AD15~8=Y,AD6~0=X //
    HX8306Insw(0x0021,Reg1);
    writeSPI(0x70,0x0022); //DDRam write enable
}
```