



晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-800480LTMQW-00H
APPROVED BY	
DATE	

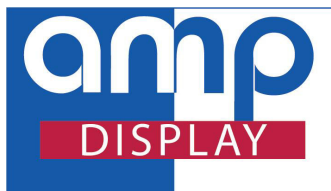
☐ Approved For Specifications

☒ Approved For Specifications & Sample

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A Brighter Solution

AMP DISPLAY INC.

SPECIFICATIONS

5.0in WVGA TFT LCD MODULE

CUSTOMER:	
CUSTOMER PART NO.	
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APPROVED FOR SPECIFICATIONS

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APPROVED FOR SPECIFICATION AND PROTOTYPES

AMP DISPLAY INC

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2008/10/08	---	New Release	Tony
2009/1/12	--	Rename to official P/N AM-800480LTMQW-00H	Edward
	4	Update the luminance to 300cd/m2 (typ.)	Edward
2009/2/11	7	Modify the LED BL diagram	Kokai
2009/5/7	3	Modify ABSOLUTE MAXIMUM RATINGS VCC	
	4	Color chromaticity	
		Modify VDD to VCC	
2009/6/19	4	Remodify Luminance	JOHN
2009/7/2	3 & 17	Correct Operating & Storage Temperature	JOHN

1. FEATURES

- (1) Construction : a-Si TFT-LCD with driving system, White LED Backlight
- (2) LCD type : Transmissive , Normally White
- (3) Number of the Colors : 16.7M colors (R,G,B 8bit digital each)
- (4) RGB Interface 45 pin.
- (5) LCD Power Supply Voltage : 3.3V single power input, built-in power supply circuit.
- (6) ROHS compliant.

2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
Display size (diagonal)	5.0	inch
Resolution	800 (W) x RGB x 480 (H)	dot
Display area	110.6 (W) x67.4 (H)	mm
Pixel pitch	0.135 (W) x 0.135 (H)	mm
Overall dimension	119.0(W) x 135.0(H) x3.2(D)	mm
Color configuration	R.G.B Vertical stripe	
Surface treatment	Antiglare(T.B.D)	
View Direction (Gray Inversion)	6 o'clock	

3. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Power Supply for logic	VCC	-0.5	5.0	V	
Input Signal Voltage	VI	-0.5	VCC + 0.5	V	(1)
Operating Temperature	Top	-20	70	°C	(2)
Storage Temperature	Tstg	-30	80	°C	

Note 1: Hsync, Vsync, DEN, DCLK, R0~R7, G0~G7, B0~B7

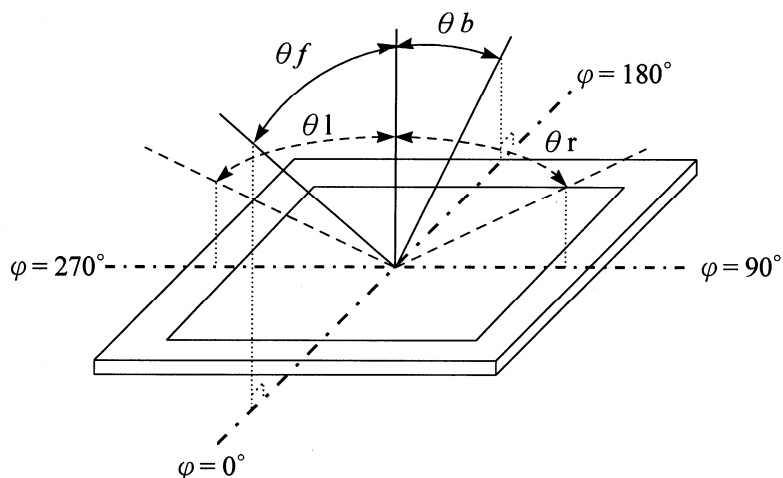
Note 2: Background color changes slightly depending on ambient temperature.
This phenomenon is reversible.

4. OPTICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle	Front	θf	$CR \geq 10$	--	70	--	deg.	(1)(2)(3)
	Back	θb		--	50	--		
	Left	θl		--	70	--		
	Right	θr		--	70	--		
Contrast ratio		CR	$\Theta = \Phi = 0^\circ$	150	250	--	--	(1)(3)
Response Time		T_r	$\Theta = \Phi = 0^\circ$	--	15	30	ms	(1)(4)
		T_f		--	35	50	ms	(1)(4)
Color chromaticity	Red	Rx	$\Theta = \Phi = 0^\circ$	0.585	0.615	0.645	--	(1)
		Ry		0.314	0.344	0.374		
	Green	Gx		0.277	0.307	0.337		
		Gy		0.532	0.562	0.592		
	Blue	Bx		0.103	0.133	0.163		
		By		0.120	0.150	0.180		
	White	Wx		0.279	0.309	0.339		
		Wy		0.320	0.350	0.380		
Luminance (ILED=120mA)		L	$\Theta = \Phi = 0^\circ$	--	450	--	cd/m ²	(1)(5)
Luminance Uniformity		ΔL	$\Theta = \Phi = 0^\circ$	70	-	-	%	(1)(5)(6)

Note 1: Ta=25°C. To be measured on the center area of panel after 10 minutes operation.

Note 2: Definition of Viewing Angle



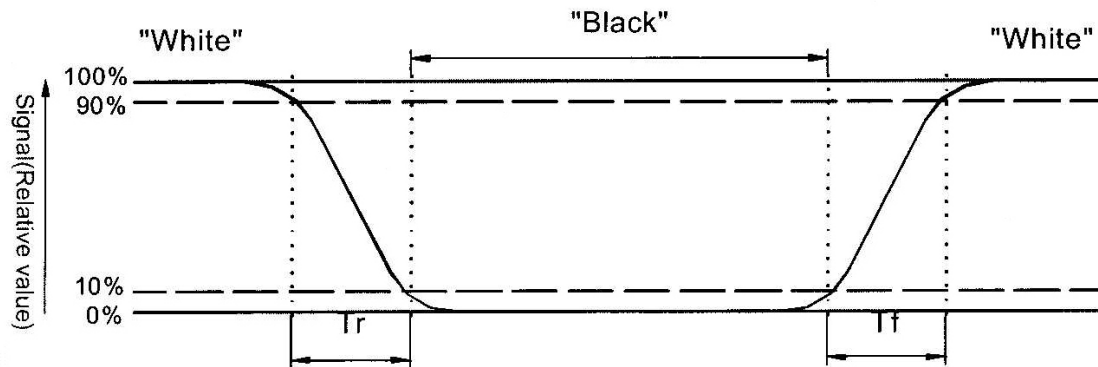
Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

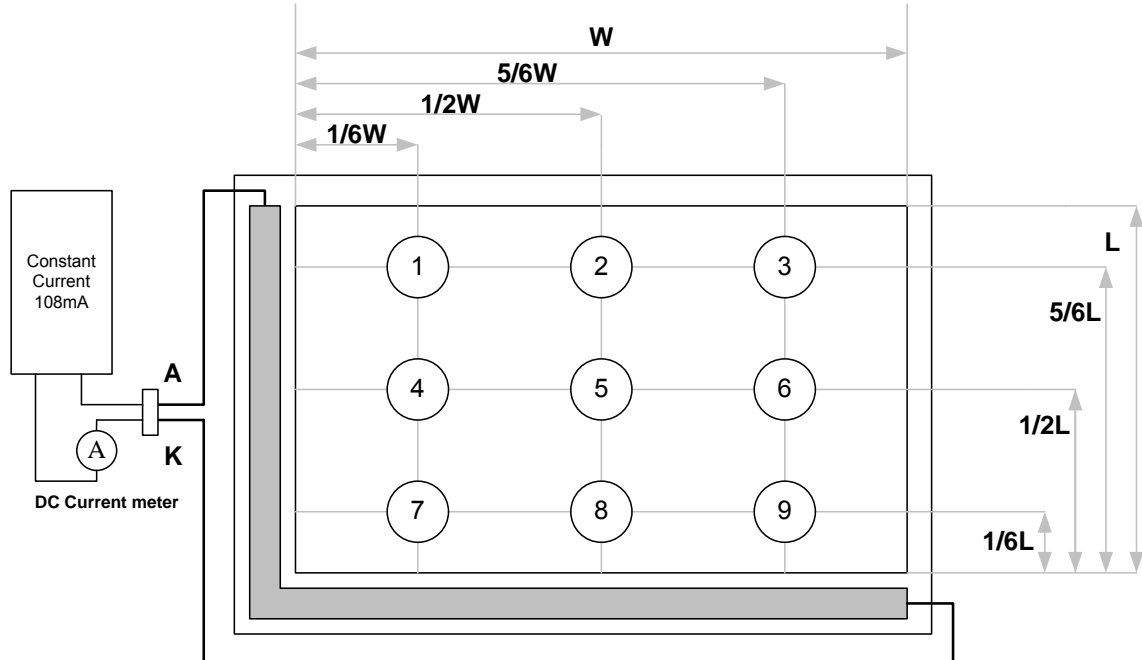
$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$$

Note 4: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time) respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 5 : Luminance is measured at point 5 of the display.



Note 6 : Definition of Luminance Uniformity

$$\Delta L = [L(\text{min.}) \text{ of 9 points} / L(\text{max.}) \text{ of 9 points}] \times 100\%$$

5. ELECTRICAL CHARACTERISTICS

5.1 LCD driving

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Power supply voltage		VCC	3.0	3.3	3.6	V	
Input voltage for logic	H Level	V _{IH}	0.7 VCC	--	VCC	V	(1)
	L Level	V _{IL}	0	--	0.3 VCC	V	
Power Supply current		IDD	--	(45)*	--	mA	(2)

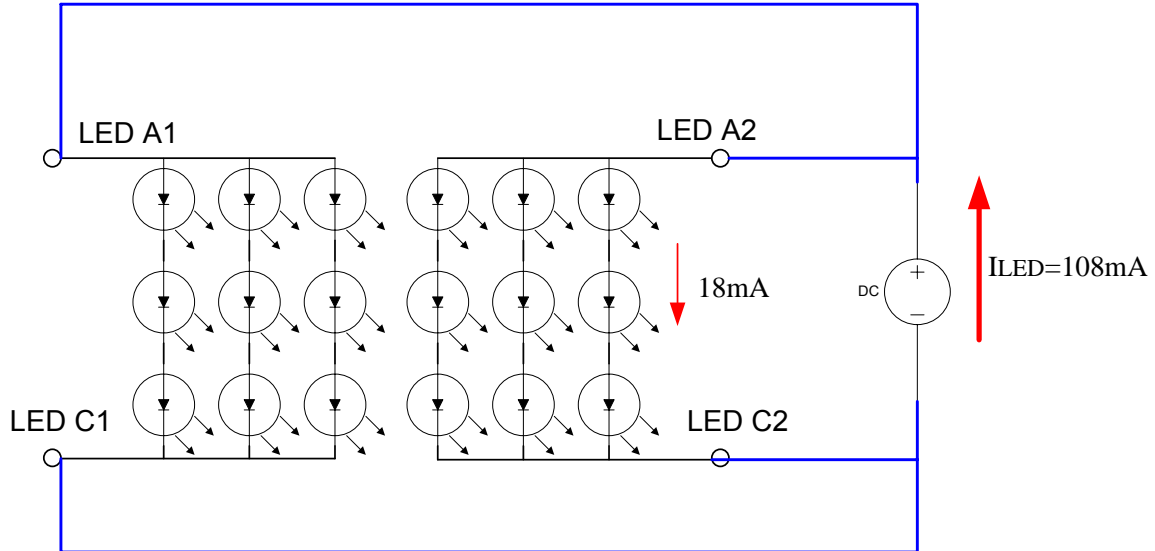
Note 1: Hsync, Vsync, DEN, DCLK, R0~R7, G0~G7, B0~B7

Note 2: fV =60Hz , Ta=25°C , Display pattern : All Black

*:Will be reference only

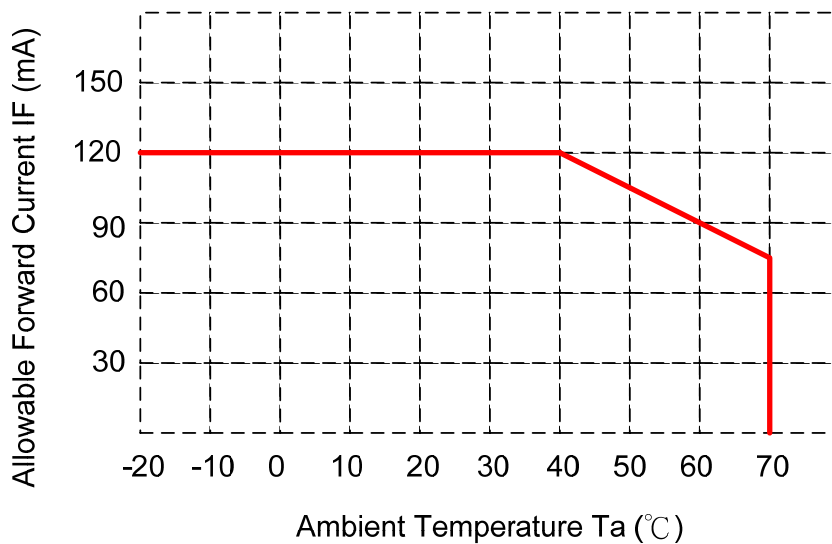
3.1 Electrical characteristic of LED Back-light

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
LED voltage	V_{AK}	--	9.9	10.8	V	$I_{LED}=108mA, T_a=25^{\circ}C$
LED forward current	I_{LED}	--	108	120	mA	$T_a=25^{\circ}C$

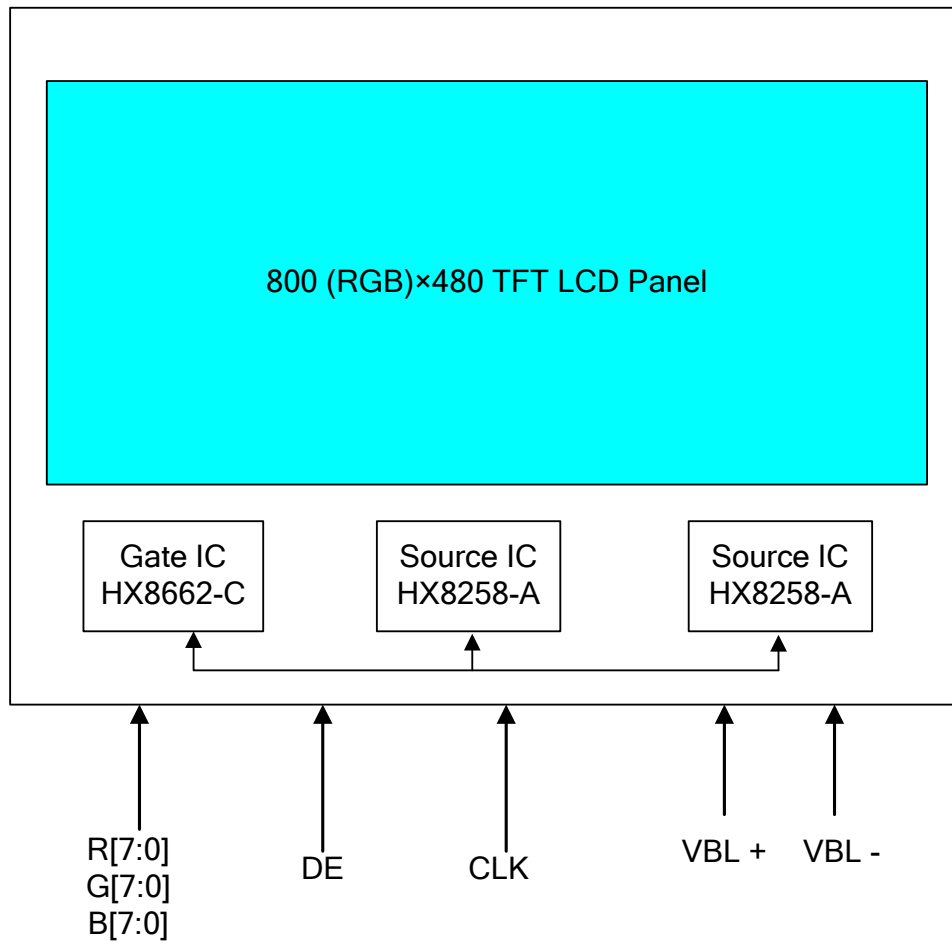


- The constant current source is needed for white LED back-light driving.

When LCM is operated over $60^{\circ}C$ ambient temperature, the I_{LED} of the LED back-light should be adjusted to 75mA max



6. BLOCK DIAGRAM



7. INTERFACE PIN ASSIGNMENT

Pin No	Symbol	Function
1	GND	Power Ground
2	GND	Power Ground
3	VCC	3.3V Power Supply for LCD
4	VCC	3.3V Power Supply for LCD
5	R0	Red Data 0 (LSB)
6	R1	Red Data 1
7	R2	Red Data 2
8	R3	Red Data 3
9	R4	Red Data 4
10	R5	Red Data 5
11	R6	Red Data 6
12	R7	Red Data 7 (MSB)
13	G0	Green Data 0 (LSB)
14	G1	Green Data 1
15	G2	Green Data 2
16	G3	Green Data 3
17	G4	Green Data 4
18	G5	Green Data 5
19	G6	Green Data 6
20	G7	Green Data 7 (MSB)
21	B0	Blue Data 0 (LSB)
22	B1	Blue Data 1
23	B2	Blue Data 2
24	B3	Blue Data 3
25	B4	Blue Data 4
26	B5	Blue Data 5
27	B6	Blue Data 6
28	B7	Blue Data 7(MSB)
29	GND	Power Ground
30	DCLK	Clock Signals
31	DISP	Display on/off (High: on, Low :off)
32	HSYNC	Horizontal SYNC signal.
33	VSYNC	Vertical SYNC signal
34	DENA	Data Enable signal (to settle the viewing area)
35	NC	No Connect
36	NC	No Connect
37	NC	No Connect
38	NC	No Connect
39	SC	Scan direction control (Low= Reverse, High= Normal)
40	GND	Power Ground
41	GND	Power Ground

42	LED C1	LED cathode 1
43	LED A1	LED anode 1
44	LED C2	LED cathode 2
45	LED A2	LED anode 2

Remark:

1. GND Pin must ground contact, can not be floating.
2. SC are controlled function

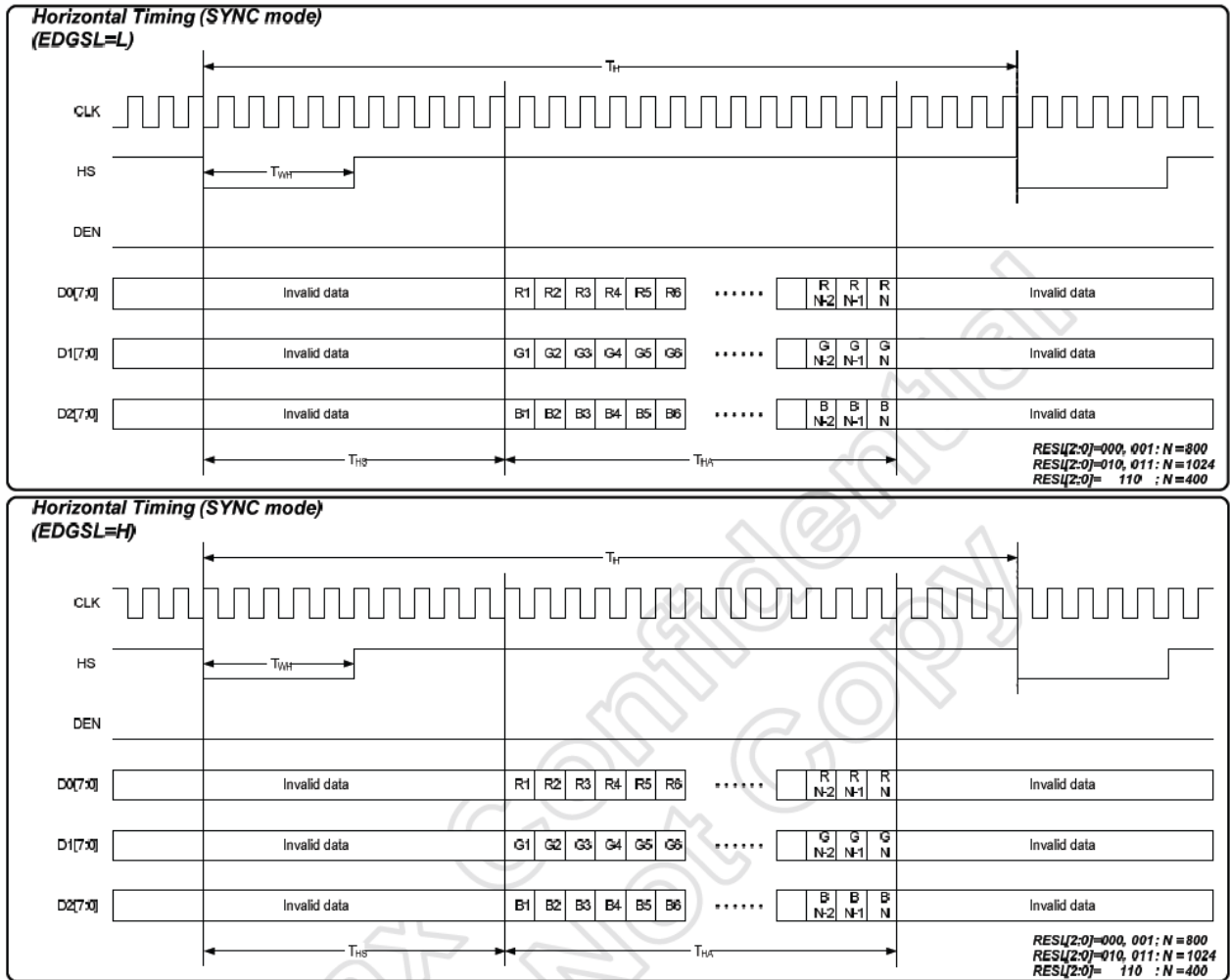
(L/R)	(U/D)	Function
1	0	Normally display
0	1	Left and Right opposite , Up and Down opposite

8. INTERFACE TIMING

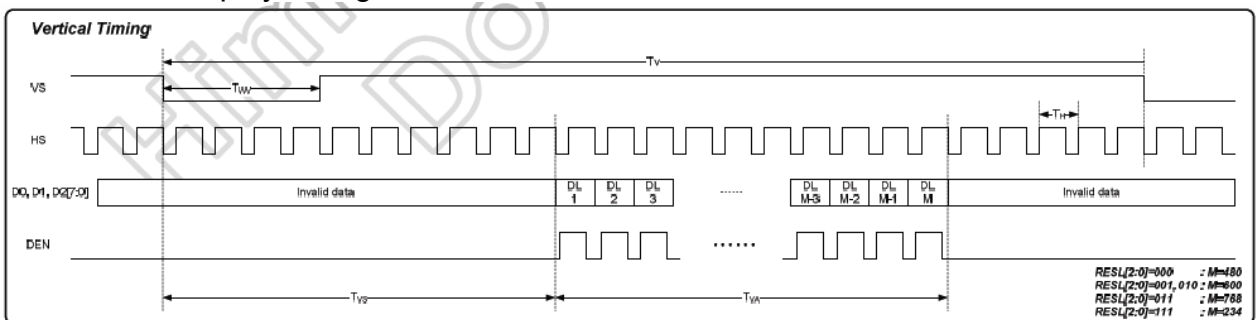
Signal	Parameter	Symbol	Min.	Typ.	Max.	Unit.
DCLK	Frequency	F_{CPH}	27.32	30.06	33.41	MHz
	DCLK period	T_{CPH}	29.93	33.26	36.59	ns
	Pulse duty	T_{CWH}	40	50	60	%
RGB DATA	Data setup time	T_{DSU}	6	-	-	ns
	Data hold time	T_{DHD}	6	-	-	ns
HSYNC	HSYNC period	T_H	950	1056	1600	T_{CPH}
	HSYNC pulse width	T_{WH}	1	128	T _{HS} -2	T_{CPH}
	HSYNC Active time	T_{HA}	-	800	-	T_{CPH}
VSYNC	VSYNC period	T_V	490	525	625	T_H
	VSYNC pulse width	T_{WV}	1	2	T _{VS}	T_H
	VSYNC Actuve time	T_{VA}		480		T_H

Remark : If DEN is fixed to low, the SYNC mode is used. Otherwise DE mode is used.
When SYNC mode is used, 1st data start from 68th CLK after HSYNC falling

- Horizontal display timing



- Vertical display timing



9. DISPLAYED COLOR AND INPUT DATA

DATA SIGNAL

COLOR		INPUT DATA																							
		R DATA								G DATA								B DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB								LSB	MSB						
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

10. QUALITY AND RELIABILITY

10.1. Test Conditions

Tests should be conducted under the following conditions :

Ambient temperature : $25 \pm 5^{\circ}\text{C}$

Humidity : $60 \pm 25\% \text{ RH}$.

10.2. Sampling Plan

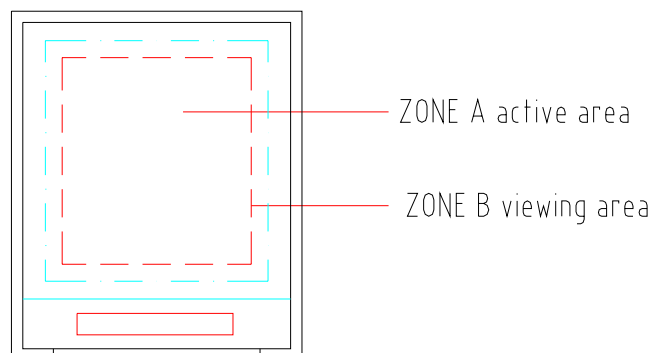
Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

10.3. Acceptable Quality Level

A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

10.4. Appearance

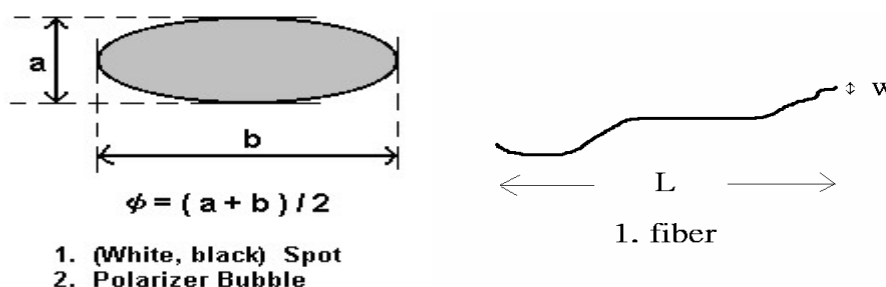
An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under florescent light. The inspection area of LCD panel shall be within the range of following limits.



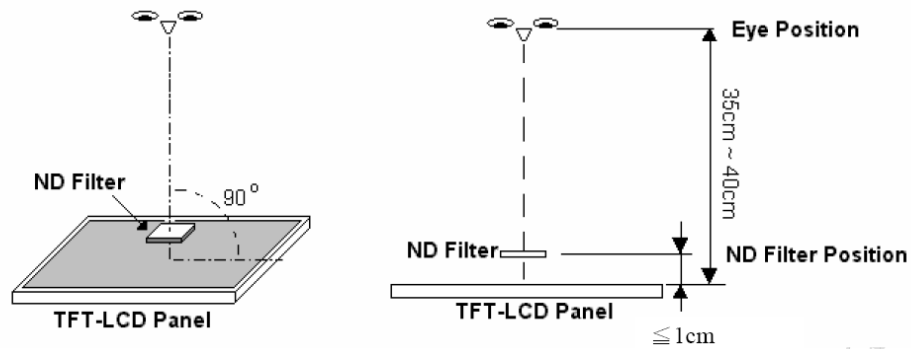
10.5. Incoming Inspection Standard

Defect Type			Limit					Note	
Visual Defect	Internal	Spot	$\varphi < 0.15\text{mm}$			Ignore		(1)	
			$0.15\text{mm} \leq \varphi \leq 0.5\text{mm}$			$N \leq 4$			
			$0.5\text{mm} < \varphi$			$N=0$			
		Fiber	$0.03\text{mm} < W \leq 0.1\text{mm},$ $L \leq 5\text{mm}$			$N \leq 3$		(1)	
			$1.0\text{mm} < W, 1.5\text{mm} < L$			$N=0$			
		Polarizer Bubble	$\varphi < 0.15\text{mm}$			Ignore		(1)	
			$0.15\text{mm} \leq \varphi \leq 0.5\text{mm}$			$N \leq 2$			
			$0.5\text{mm} < \varphi$			$N=0$			
		Mura	It' OK if mura is slight visible through 6%ND filter						
Electrical Defect	Bright Dot		A Grade			B Grade			
			C Area	O Area	Total	C Area	O Area	Total	(3)
			$N \leq 0$	$N \leq 2$	$N \leq 2$	$N \leq 2$	$N \leq 3$	$N \leq 5$	(2)
	Dark Dot		$N \leq 2$	$N \leq 3$	$N \leq 3$	$N \leq 3$	$N \leq 5$	$N \leq 8$	
	Total Dot		$N \leq 4$			$N \leq 5$	$N \leq 6$	$N \leq 8$	(2)
	Two Adjacent Dot		$N \leq 0$	$N \leq 1$ pair	$N \leq 1$ pair	$N \leq 1$ pair	$N \leq 1$ pair	$N \leq 1$ pair	(4)
	Three or More Adjacent Dot		Not Allowed						
	Line Defect		Not Allowed						

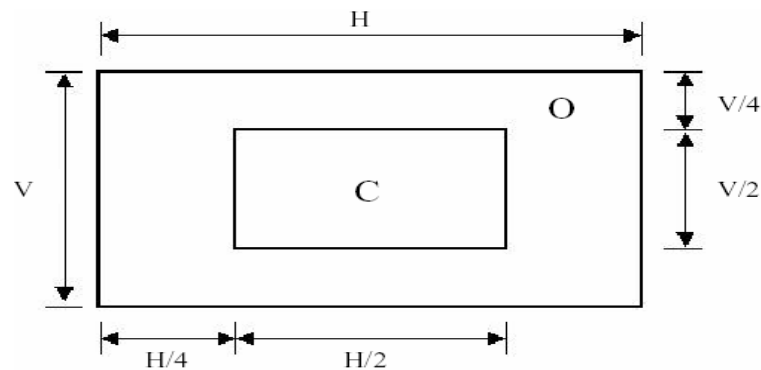
[Note1] W : Width[mm], L : Length[mm], N : Number, ϕ : Average Diameter



[Note2] Bright dot is defined through 6% transmission ND Filter as following.



[Note3]

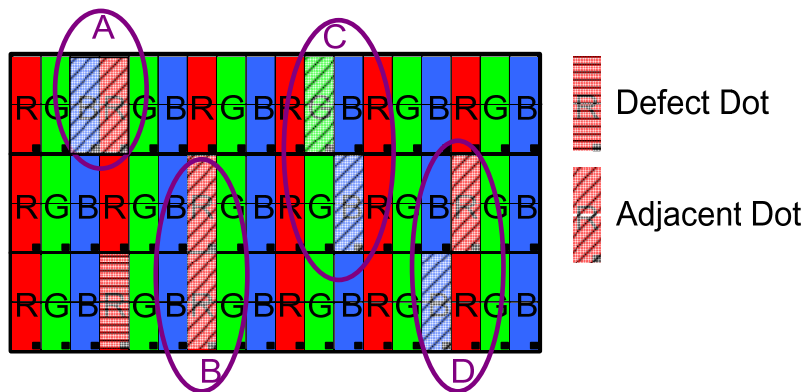


C Area: Center of display area

O Area: Outer of display area

[Note4]

Judge defect dot and adjacent dot as following. Allow below (as A, B, C and D status) adjacent defect dots, including bright and dark adjacent dot. And they will be counted 2 defect dots in total quantity.



- (1) The defects that are not defined above and considered to be problem shall be reviewed and discussed by both parties.
- (2) Defects on the Black Matrix, out of Display area, are not considered as a defect or counted.

10.6. Reliability Test

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=96 hrs	
Low Temperature Operation	-20±3°C , t=96 hrs	
High Temperature Storage	80±3°C , t=96 hrs	1,2
Low Temperature Storage	-30±3°C , t=96 hrs	1,2
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 m in. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2
Humidity Test	40 °C, Humidity 90%, 96 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

11. USE PRECAUTIONS

11.1. Handling precautions

- 1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- 2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzene and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- 3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- 4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2. Installing precautions

- 1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- 2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- 3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- 4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3. Storage precautions

- 1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- 2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- 3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

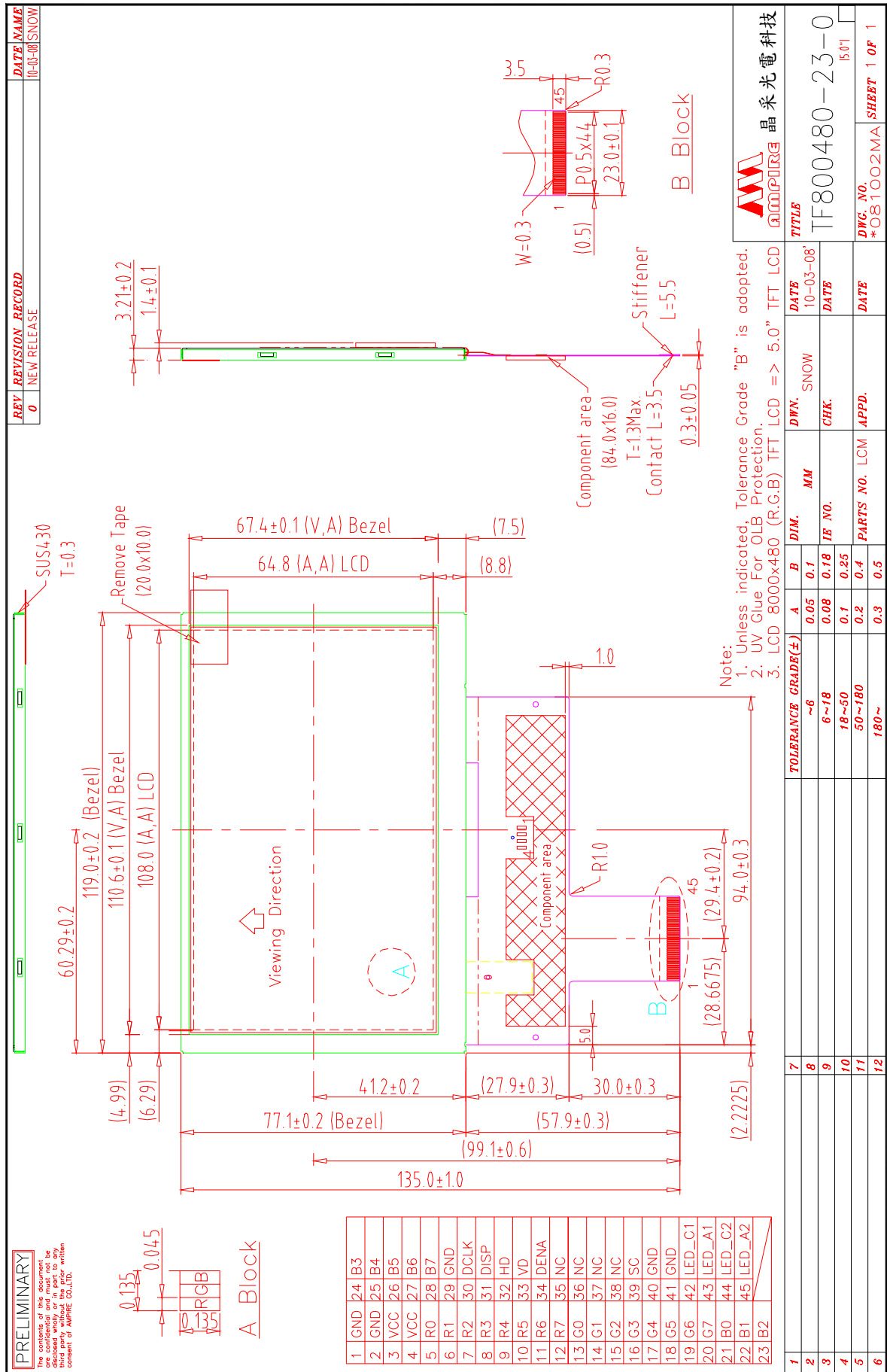
11.4. Operating precautions

- 1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- 2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- 3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC drive voltage.
- 4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- 5) Make certain that each signal noise level is within the standard (L level: 0.2VCC or less and H level: 0.8VCC or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- 6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- 7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
- 8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

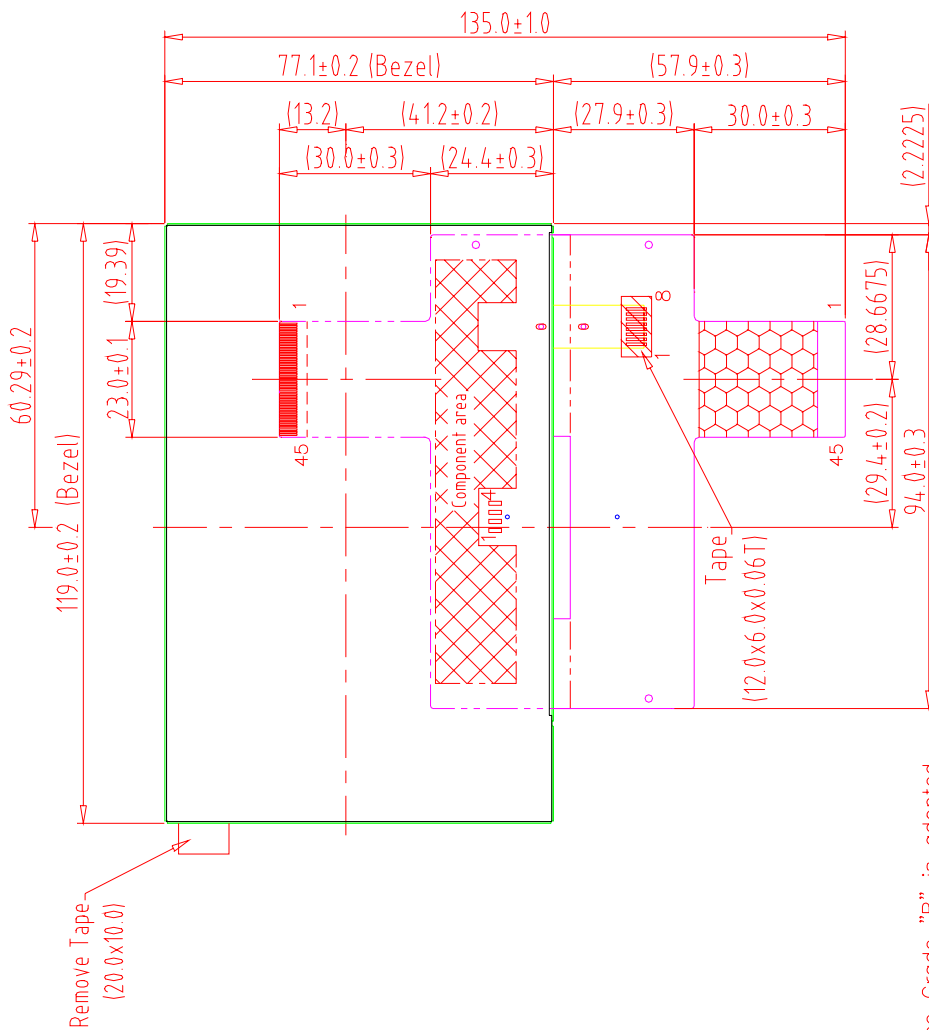
11.5. Other

- 1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- 2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
- 3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

11. OUTLINE DIMENSION



PRELIMINARY The contents of this document are confidential and must not be disclosed in any form without the prior written consent of AMPRE CO.,LTD.		REV. REVISION RECORD		DATE NAME	
		0	NEW RELEASE	10-03-08	SNOW



- Note:
1. Unless indicated, Tolerance Grade "B" is adopted.
 2. UV Glue For OLB Protection.
 3. LCD 8000x480 (R.G.B) TFT LCD => 5.0" TFT LCD

AMPRE 晶采光電科技		TITLE		DATE	
		TF800480-23-0		10-03-08	
				DATE	
				CHK.	
				PARTS NO. LOW-1	
				APPD.	
				DATE	
				DWC. NO.	
				*081003MA	
				SHEET 1 OF 1	