

# AMP DISPLAY INC.

# **SPECIFICATIONS**

# 5.7-in COLOR TFT MODULE

CUSTOMER:	
CUSTOMER PART NO.	
AMP DISPLAY PART NO.	AMP DISPLAY
APPROVED BY:	
DATE:	

APPROVED FOR SPECIFICATIONS

APPROVED FOR SPECIFICATION AND PROTOTYPES

# AMP DISPLAY INC

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## **RECORD OF REVISION**

<b>Revision Date</b>	Page	Contents	Editor
2007/12/5	-	New Release	Edward

# 1 Features

5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 5.7" TFT-LCD panel, a driver circuit and backlight unit.

(1) Construction: 5.7" a-Si color TFT-LCD, White LED Backlight and PCB.

- (2) Resolution (pixel): 320(R.G.B) X240
- (3) Number of the Colors : 262K colors ( R , G , B 6 bit digital each)
- (4) LCD type : Transmissive Color TFT LCD (normally White)
- (5) Interface: 33 pin
- (6) Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.
- (7) Viewing Direction: 6 O'clock ( The direction it's hard to be discolored )

# 2 Physical specifications

Item	Specifications	Unit
Display resolution(dot)	960 (W) x 240(H)	mm
Active area	115.2 (W) x 86.4 (H)	mm
Screen size	5.7(Diagonal)	mm
Pixel size	120 (W) x 360 (H)	um
Color configuration	R.G.B stripe	
Overall dimension	131.0(W)x102.2(H)x10.9(D)	mm
Weight	T.B.D	mg
Backlight unit	LED	

# 3 Electrical specification

#### 3.1 Absolute max. ratings

#### 3.1.1 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VCC	VSS=0	-0.3	6.0	V	
Input voltege	V <sub>in</sub>		-0.3	VCC+0.3	V	Note 1

Note1:Hsync, Vsync, ENAB, CK, R0~R5, G0~G5, B0~B5

	OPER	ATING	STOF	RAGE	
Item	MIN	MAX	MIN	MAX	Remark
Temperature	-20	70	-30	80	Note2,3,4,5,6,7,8
Humidity	No	Note1		te1	
Corrosive Gas	Not Acc	eptable	Not Acceptable		

3.1.2 Environmental Absolute max. ratings

Note1 : Ta <= 40°C : 85% RH max

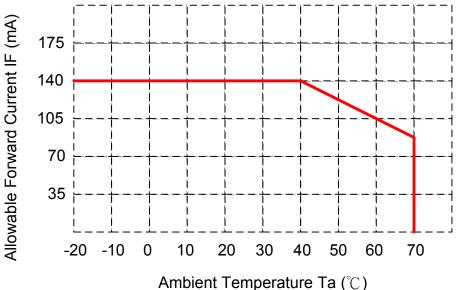
Ta >  $40^{\circ}$ C : Absolute humidity must be lower than the humidity of

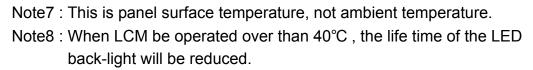
85%RH at 40°C

Note2 : For storage condition Ta at  $-30^{\circ}C < 48h$ , at  $80^{\circ}C < 100h$ 

For operating condition Ta at -20°C < 100h

- Note3 : Background color changes slightly depending on ambient temperature. This phenomenon is reversible.
- Note4 : The response time will be slower at low temperature.
- Note5 : Only operation is guarantied at operating temperature. Contrast , response time, another display quality are evaluated at +25°C
- Note6 : When LCM is operated over 40°C ambient temperature, the I<sub>LED</sub> of the LED back-light should be follow :





#### 3.2 Electrical characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark	
Power supp	VCC	3.0	3.3	3.6	V		
Input Voltage	H Level	V <sub>IH</sub>	0.7 VCC	-	VCC	V	Note 1
for logic	L Level	VIL	0	-	0.3 VCC	V	NOLE I
Power Supply c	ICC		130	160	mA	Note 2	

# 3.2.1 DC Electrical characteristic of the LCD

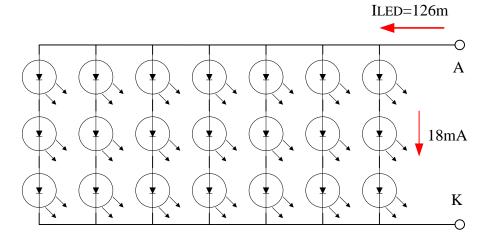
Typical operting conditions (VSS=0V)

Note1: Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

Note2: fV =60Hz , Ta=25°C , Display pattern : All Black

#### 3.2.2 Electrical characteristic of LED Back-light

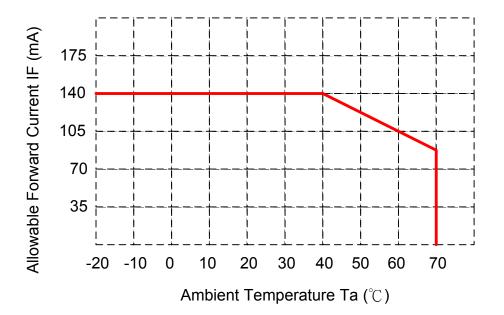
Paramenter	Symbol	Min.	Тур.	Max.	Unit	Condiction		
LED voltage	V <sub>ak</sub>	8.4	9.6	10.8	V	I <sub>LED</sub> =140mA,Ta=25°C		
LED forward current	I <sub>LED</sub>		126	140	mA	Ta=25°C		
	I <sub>LED</sub>		84	105	mA	Ta=60°C		
Lamp life time		30,000	-	-	Hr	I <sub>LED</sub> =140mA,Ta=25°С		



The constant current source is needed for white LED back-light driving.

When LCM is operated over 60°C ambient temperature, the  $\mathsf{I}_{\mathsf{LED}}$  of the LED

back-light should be adjusted to 105mA max

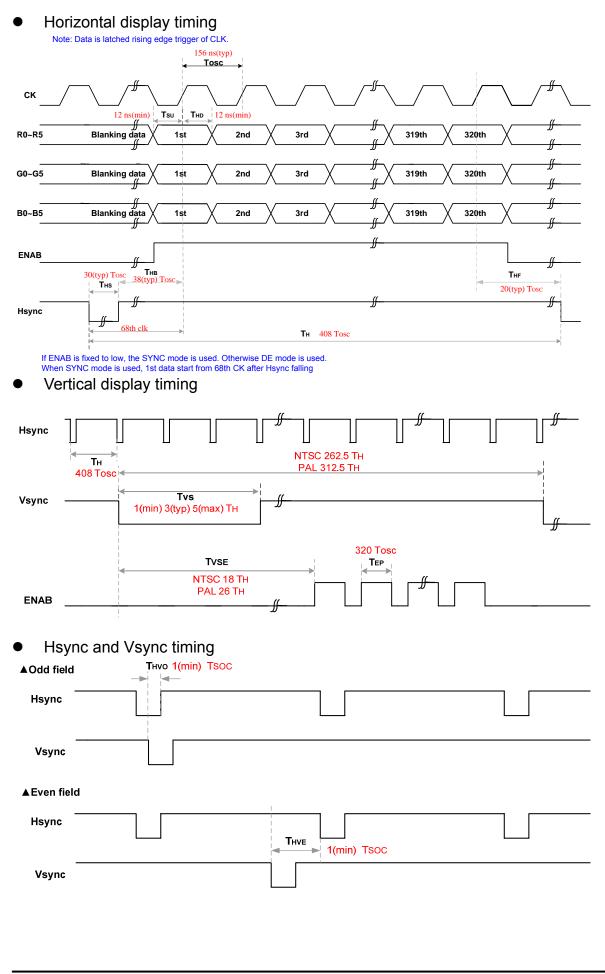


# 3.3 AC Timing characteristic of the LCD

Signal	Parameter		Symbol	Min.	Тур.	Max	Unit.	Remark
DCLK	DCLK period	Tosc	-	156	-	ns		
	Frequency		Fosc	-	6.4	-	MHz	
	DCLK High plus wid	th	Тсн	-	78	-	ns	
	DCLK Low plus widt	h	TCL	-	78	-	ns	
RGB	Data setup time		Tsu	12	-	-	ns	
DATA	Data hold time		THD	12	-	I	ns	
Hsync	Hsync period		Τн	-	408	-	Tosc	
	Hsync pulse width		THS	5	30	-	Tosc	
	Back-Parch		Тнв		38		Tosc	
	Front-Parch		THF		20		Tosc	
	Hsync rising time		TCr	-	-	700	ns	
	Hsync falling time		TCf	-	-	300	ns	
Vsync	Vsync period	NTSC		-	262.5	-	Тн	
	v sync penou	PAL		-	312.5	-	Тн	
	Vsync pulse width		Tvs	1	3	5	Тн	
	Back-Porch	NTSC	Тvв		15		Тн	
		PAL	170		23		Тн	
	Display Period		TVD		240		Тн	
	Front Porch	NTSC	TVF		4.5		Тн	
		PAL			46.5		Тн	
	Vsync rising time		T∨r	-	-	700	ns	
	Vsync falling time		TVf	-	-	1.5	μs	
	Vsync falling to Hsy time for odd field	•	Тнуо	1	-	-	Tosc	
	Vsync falling to Hsy time for even field		THVE	1	-	-	Tosc	
ENAB	Vsync-DEN time	NTSC	TVSE	-	18	-	Тн	
	-	PAL	TVSE	-	26	-	Тн	
	Hsync-DEN time		THE	36	68	88	Tosc	
	DEN plus width		TEP	-	320	-	Tosc	

#### a. Timing condition

Note : If ENAB is fixed to low, the SYNC mode is used. Otherwise DE mode is used. When SYNC mode is used, 1st data start from 68th CK after Hsync falling



# 4 Optical specification

Item		Symbol	Conditon	Min.	Тур.	Max.	Unit	Remark
Response	Rise	Tr	Θ=0°	-	15	30	ms	Noto 1 2 2 5
Time	Fall	Τ <sub>f</sub>		-	35	50	ms	Note 1,2,3,5
Contrast	ratio	CR	At optimized viewing angle	200	350	-		Note 1,2,4,5
Viewing Angle	Top Botto m Left Right		CR≧10	55 45 55 55	60 50 60 60	- - -	deg.	Note1,2, 5,6
Brightne	Drichterse		l <sub>LED</sub> =126mA, 25℃	427.5	450	-	cd/m²	Note 7
Digitite	.55	ΥL	l <sub>LED</sub> =140mA, 25℃	475	500	-	cd/m²	Note 1
Pod chrom	oticity	XR		0.610	0.640	0.670		Niete 7
Red chrom	allolly	YR		0.314	0.344	0.374		Note 7
Croop obrop	a oti oitu	XG		0.268	0.298	0.328		For reference
Green chron	Green chromaticity		Θ=0°	0.553	0.583	0.613		only. These data should
Blue chromaticity		Хв	Θ=0°	0.102	0.132	0.162		
		Yв		0.107	0.137	0.167		be update
White obrem	White chromaticity			0.282	0.312	0.342		according the prototype.
white childh	allolly	Yw		0.299	0.329	0.359		prototype.

#### 4.1 Optical characteristic of the LCD

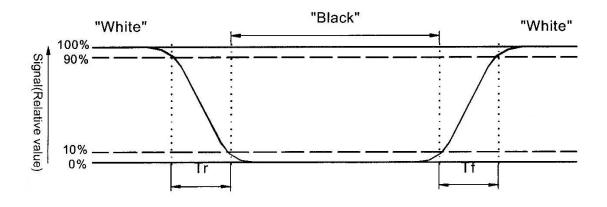
( )For reference only. These data should be update according the prototype. Note 1: Ambient temperature=25 $^{\circ}$ C, and lamp current I<sub>LED</sub>=140mA.To be

measured in the dark room.

Note 2:To be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-7,after 10 minutes operation.

Note 3.Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from"white" to "black" (rising time),respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4.Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

Contrast ratio(CR)= Photo detector output when LCD is at "White" state Photo detector Output when LCD is at "Black" state

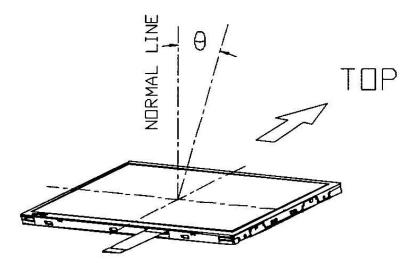
#### Note 5:White $V_i = V_{i50} + 1.5V$

Black V<sub>i</sub>=V<sub>i50</sub>+2.0V

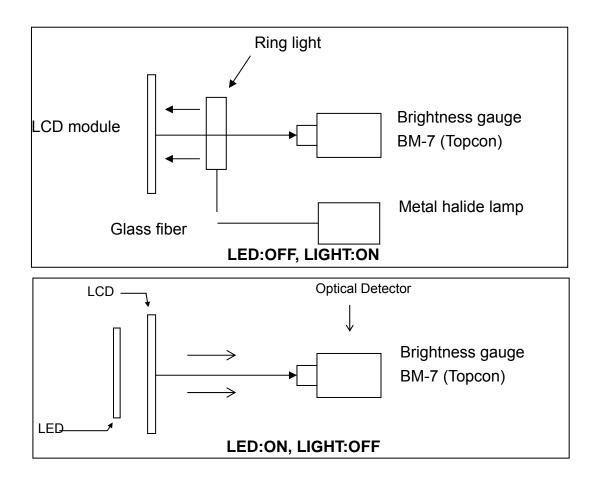
"±"means that the analog input signal swings in phase with V<sub>COM</sub> signal. " $\frac{1}{+}$ " means that the analog input signal swings out of phase with V<sub>COM</sub> signal.

 $V_{i50}$ : The analog input voltage when transmission is 50%. The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note 6.Definition of viewing angle,Refer to figure as below.



Note 7.Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.



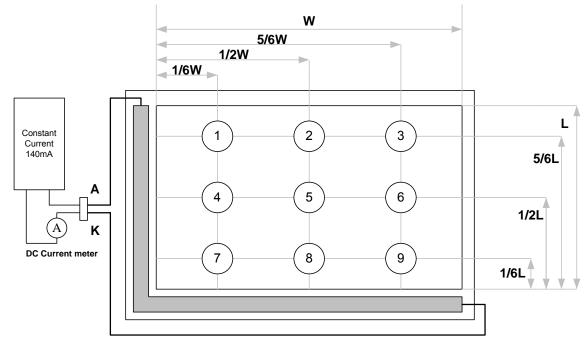
#### 4.2 Optical characteristic of the Back-light

ITEM	MIN	TYP	MAX	UNIT	Condition
Bare Brightness	3500			Cd/m2	I <sub>LED</sub> =140mA,Ta=25°C
AVG. X of 1931 C.I.E.	0.28	0.31	0.34		I <sub>LED</sub> =140mA,Ta=25°C
AVG. Y of 1931 C.I.E.	0.28	0.31	0.34		I <sub>LED</sub> =140mA,Ta=25°C
Brightness Uniformity	80			%	I <sub>LED</sub> =140mA,Ta=25°C

()For reference only. These data should be update according the prototype.

Note1 : Measurement after 10 minutes from LED BL operating.

Note2 : Measurement of the following 9 places on the display.



Note3: The Uniformity definition (Min Brightness / Max Brightness) x 100%

# 5 Interface specifications

Driving signals for the TFT panel								
Symbol	I/O	Description	Remark					
GND		Gound						
CK		Clock signal. Latching data at the rising edge						
Hsync		Horizontal sync input in digital RGB mode						
Vsync		Vertical sync input in digital RGB mode						
GND	I	Gound						
R0	Ι							
R1	Ι							
R2	Ι	Pod doto						
R3	Ι							
R4	Ι							
R5	I							
GND	I	Gound						
G0	Ι							
G1	Ι							
G2	Ι	Green data						
G3	Ι							
G4	Ι							
G5	Ι							
GND	Ι	Gound						
B0	-							
B1	-							
B2	Ι	Plue data						
B3								
B4	Ι							
B5	Ι							
GND		Gound						
ENAB	Ι	Input data enable control						
VCC	Ι	+3 3V/ Power Supply						
VCC	Ι							
R/L	Ι	Right / Left Reveres Mode						
U/D	I	Up / Down Reveres Mode						
NC	I	Not use						
GND		Gound						
	GND         CK         Hsync         Vsync         GND         R0         R1         R2         R3         R4         R5         GND         G0         G1         G2         G3         G4         G5         GND         B0         B1         B2         B3         B4         B5         GND         ENAB         VCC         R/L         U/D         NC	GND         CK         Hsync         Vsync         GND         R0         R1         R2         R3         R4         R5         GND         GND         R3         R4         R5         GND         G1         G2         G3         G4         G5         GND         B0         B1         B2         B3         B4         B5         GND         B4         B5         GND         B4         B5         I         VCC         R/L         VCC         R/L         NC	GND         Gound           CK         Clock signal. Latching data at the rising edge           Hsync         Horizontal sync input in digital RGB mode           Vsync         Vertical sync input in digital RGB mode           GND         I           R0         1           R1         1           R2         1           R4         1           R5         1           GND         I           Gound         Gound           G0         1           R5         1           Gound         Gound           G0         1           G1         1           G2         1           Green data         Genen data           G3         1           G4         1           G5         1           GND         1           B1         1           B2         1           B3         -           B4         1           B5         1           GND         1           Gound         -           B3         -           B4         1					

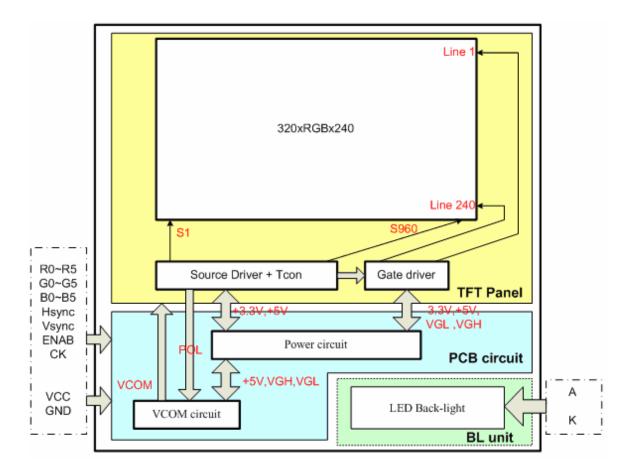
#### 5.1 Driving signals for the TFT panel

#### 5.2 Driving signals for the LED back-light

Pin no	Symbol	Level	Description	Remark
1	А	-	LED Anode	
2	NC	-	No connection	
3	K	-	LED Cathode	

#### JST Housing: BHR-03VS-1

# 6 BLOCK DIAGRAM



#### Color & DATA SIGNAL Gray Scale R5 R4 R3 R2 R1 R0 G5 G4 G3 G2 G1 G0 B5 B4 B3 B2 B1 B0 Black Red(0) Green(0) Basic Blue(0) Color Cyan Magenta Yellow White Black Red(62) Red(61) Red Red(31) : Red(1) Red(0) Black Green(62) Green(61) Green Green(31) Green(1) Green(0) Black Blue(62) Blue(61) Blue Blue(31) Blue(1) Blue(0)

# 7 DISPLAYED COLOR AND INPUT DATA

# 8 QUALITY AND RELIABILITY

#### 8.1 TEST CONDITIONS

Tests should be conducted under the following conditions : Ambient temperature :  $25 \pm 5^{\circ}C$ Humidity :  $60 \pm 25\%$  RH.

### 8.2 SAMPLING PLAN

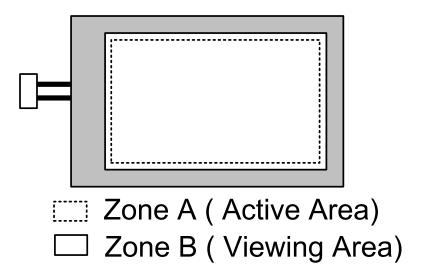
Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

# 8.3 ACCEPTABLE QUALITY LEVEL

A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

## 8.4 APPEARANCE

An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under flourescent light. The inspection area of LCD panel shall be within the range of following limits.



#### 8.5 INSPECTION QUALITY CRITERIA

No.	ltem	Criterior	Defect type		
1	Non display	No non display is allowed			Major
2	Irregular operation	No irregular operation is allowed			Major
3	Short	No short are allowed			Major
4	Open	Any segments or common patterns that don't activate are rejectable.			Major
5	Black/White spot (I)	Size D (mm)Ac $D \le 0.15$ 0.15 $0.15 < D \le 0.20$ 0.20 $0.20 < D \le 0.30$ 0.30 $0.30 < D \le 0.5$		ptable number Ignore 3 2 1	Minor
6	Black/White line (I)	$\begin{tabular}{ c c c c c c } \hline Length(mm) & Acceptable number \\ \hline 10 < L & 0.03 < W \le 0.04 & 5 \\ \hline 5.0 < L \le 10 & 0.04 < W \le 0.06 & 3 \\ \hline 1.0 < L \le 5.0 & 0.06 < W \le 0.07 & 2 \\ L \le 1.0 & 0.07 < W \le 0.09 & 1 \\ \hline \end{tabular}$		Minor	
7	Black/White sport (II)	Size D (mm)Acceptable num $D \le 0.30$ Ignore $0.30 < D \le 0.50$ 5 $0.50 < D \le 1.20$ 3 $1.20 < D$ 0		Ignore 5 3	Minor
8	Black/White line (II)	$\begin{tabular}{ c c c c c c c } \hline Length (mm) & Width (mm) & Acceptable number \\ \hline 20 < L & 0.05 < W \le 0.07 & 5 \\ 10 < L \le 20 & 0.07 < W \le 0.09 & 3 \\ 5.0 < L \le 10 & 0.09 < W \le 0.10 & 2 \\ L \le 5.0 & 0.10 < W \le 0.15 & 1 \\ \hline \end{tabular}$			Minor
9	Back Light	1. No Lighting is rejectabl 2. Flickering and abnorm	Major		
10	Display pattern	$\frac{A+B}{2} \le 0.30  0 < C$ Note: 1. Acceptable up to 3 2. NG if there're to two	Minor		

	Blemish &					
11	Foreign matters	Size D (mm)		AC	ceptable number	
	Size:	$D \le 0.15$			Ignore	Minor
		0.15 < D <u>&lt;</u> 0.20 0.20 < D < 0.30			3 2	-
	$D = \frac{A+B}{2}$	0.20 < D <u>&lt;</u> 0.30 0.30 < D ≦ 0.5			1	
	2	$0.30 \lor D = 0.3$	<u>0.30 &lt; D ≅ 0.5</u>			
	Soratah an	Width (mm)Length (mm)Acceptable number				
	Scratch on Polarizer	W <u>&lt;</u> 0.03 Igno				
10		0.03 <w<u>&lt;0.05</w<u>	L > 2.0 1		Ignore	Minor
12	. A	0.05 -14/-0.09			1	
		0.05 <w<u>&lt;0.08</w<u>	L > 1 L <u>&lt;</u> 1		Ignore	
		0.08 <w< td=""><td></td><td></td><td>Note(1)</td><td></td></w<>			Note(1)	
		0.08 <w< th="">Note (1)Note(1)Note(1) Regard as a blemish</w<>				
		Size D (r	,	Ac	ceptable number	
13	Bubble in		D <u>&lt;</u> 0.20		Ignore	Minor
	polarizer	0.20 < D < 0.50 3				
		0.50 < D <u>&lt;</u> 0.80 0.80 < D	)	2		
		0.00 < D				
	Stains on					
14	LCD panel	Stains that car	Minor			
	surface	with a soft clot				
15	Rust in Bezel	Rust which is visible in the bezel is rejectable.				Minor
	Defect of					
10	land surface					
16	contact (poor	Evident crevic	Minor			
	soldering)					
		1 Eailure to mount parts				Major
17	Parts	<ol> <li>Failure to mount parts</li> <li>Parts not in the specifications are mounted</li> </ol>			Major	
	mounting	3. Polarity, for	Major			
	1. LSI, IC lead width is more than 50% b					Minor
	Parts	outline.				
18	alignment	2. Chip compo	Minor			
	angrinterit	the leads is	WIITO			
		1. 0.45<φ ,N≧1				Major
	Conductive	1. $0.45 < \phi$ , $N \le 1$ 2. $0.30 < \phi < 0.45$ , $N \ge 1$				Minor
19	foreign matter	φ:Average				
	(Solder ball,	3. 0.50 <l< td=""><td>Minor</td></l<>	Minor			
	Solder chips)	L: Average				
		1. Due to PCB	copper fo	il pattern	burnout, the pattern is	
		connected	Minor			
20	Faulty PCB correction	places are				
	CONCOUNT	2. Short circuited part is cut, and no resist coating has				Minor
		been performed.				

21	Defect Dot	The TFT panel may have bright dot or Dark dot. The acceptable number defection:					
		Bright dot	Dark dot	Total dot	Distance between Dark dark		Minor
		2	4	6	$L \ge 5 \text{ mm}$		

#### 8.6 RELIABILITY

Test Item	Test Conditions				
High Temperature Operation	70±3°C , t=96 hrs				
Low Temperature Operation	-20±3°C , t=96 hrs				
High Temperature Storage	80±3°C , t=96 hrs	1,2			
Low Temperature Storage	-30±3°C , t=96 hrs	1,2			
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 m in. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2			
Humidity Test	40 °C, Humidity 90%, 96 hrs	1,2			
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis				

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions

 $(15-35^{\circ}C, 45-65\% RH).$ 

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

## 9 USE PRECAUTIONS

#### 9.1 Handling precautions

- 1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- 2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- 3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- 4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

#### 9.2 Installing precautions

- 1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx.  $1M\Omega$  and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- 2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- 3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- 4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

#### 9.3 Storage precautions

- Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- 2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.

3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

#### 9.4 Operating precautions

- 1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- 2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
- 4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- 5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- 6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- 7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
- 8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

#### 9.5 Other

- 1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- 2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
- 3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.



